

Improving Bulk Driven CMOS OTA Performance using Feedforward Technique

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Received: June 2017

Revised: July 2017

Accepted: July 2017

ABSTRACT:

This paper presents an ultra-low-voltage ultra-low-power CMOS Operational Trans conductance Amplifier (OTA), using a novel feed-forward technique. The proposed topology is based on a bulk driven input differential pair, and inclusion of a gain-stage in the Miller capacitor feedback path to enhance the pole-splitting effect. The main objective of the proposed OTA is to utilize idle current source devices in small signal paths to improve the OTA performance. Not only the DC gain of the proposed OTA is enhanced by 10dB, its unity gains bandwidth (UGB) is also increased by factor of 3 with no extra power dissipation. The proposed OTA is designed and simulated in 180nm CMOS technology consuming only 386nW under the supply voltage of 400mV.

KEYWORDS: OTA, Low-power design, Bulk driven transistor, Pole splitting, Feedforward.

1. INTRODUCTION

The design of high performing analog integrated circuits that operate at low supply voltages is becoming increasingly important, particularly for applications such as medical electronic implants, as well as battery-powered portable electronic devices. The operational transconductance amplifier (OTA) still forms a vital analog building block of such devices, and is the largest and most power consuming component in many of these applications [6].

Bulk driven MOSFET is one of the interesting approaches for low voltage analog circuits design. An inversion layer can be formed by assigning an adequate value to the gate to source voltage of a MOSFET, then applying the input signal to the bulk terminal. Therefore, the threshold voltage of a MOSFET can be reduced, or even removed from the signal path. The most important drawbacks of employing the bulk-driven method is to have a low DC gain and an extremely low unity gain bandwidth (UGB) owing to the fact that the body transconductance, g_{mb} , is approximately five times smaller than the gate transconductance, g_m [1].

A 0.4V, 386nW sub-threshold CMOS OTA is proposed in this paper. By using a new feed-forward technique, the DC gain and the unity gain bandwidth of the proposed OTA have been considerably increased to 88dB and 86 kHz, respectively.

2. PROPOSED CMOS OTA

Fig.1 illustrates the schematic of the proposed OTA which employs a feed-forward technique. As can be seen from the Fig.1, M_{3a} - M_{3b} and M_{4a} - M_{4b} form composite transistors. They are capable of biasing differential pair and common gate amplifier with the same value; thus there is no need to use additional biasing circuit. V_{ds3} of the composite transistor can be given by (1) [2].

$$V_{ds3a} = \frac{KT}{q} \ln\left(1 + 2 \frac{(W/L)_{3b}}{(W/L)_{3a}}\right) \quad (1)$$

In terms of dc analysis, voltages V_{ds3a} and V_{ds4a} are constant and equal. Hence, voltages V_{ds1} and V_{ds2} need to be constant and equal to optimize the matching of differential pair M_1 and M_2 , and to reduce the differential offset voltage. Equation (2) should be satisfied to prevent no systematic offset in the proposed circuit

$$(W/L)_6 = \frac{2(W/L)_{3a}(W/L)_7}{2(W/L)_8 + (W/L)_5} \quad (2)$$

In low frequency applications, flicker noise plays a tangible role on the performance of the circuit. To minimize the effect of this noise, the transistors sizes should be chosen to be rather large in the sub-threshold operation. In addition, the transistors operate in the weak inversion region, and since their role was illustrated by the authors in detail in [3], they will not be discussed here.

compared to that of the conventional OTA and its value is as low as $20nV/\sqrt{Hz}$ @10mHz. The designed system demonstrates a relatively suitable response in different process corners. The slew rate of the proposed OTA is 25mV/s. The OTAs drive a capacitive load of 15pF, and to compensate the frequency response, the miller capacitor is chosen to be 2.5pF. It is worth mentioning that the DC gain, unity gain bandwidth, and noise performance of the proposed OTA has improved without consuming extra power and deteriorating other OTA parameters and in fact, the applied technique is the simplest and the most efficient method to improve the OTA performance. The whole performance of the OTAs is tabulated in Table 1. A figure of merit (FOM) is used to evaluate the OTA performance [3]:

$$FoM = \frac{(Gain)(Unity\ gain\ freq.)}{(Power\ supply)(Power\ consumption)} \quad (6)$$

As can be seen from the Table 1, a considerable FOM is produced for the proposed OTA while the supply voltage is as low as 0.4V. Considering the slew rate and power spectral density of input referred noise of the OTA, a new FOM can be defined in which the efficiency of the proposed OTA can be highlighted

better.

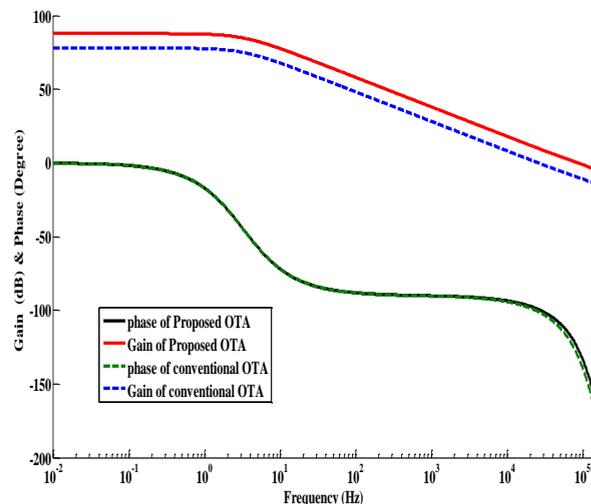


Fig. 2. simulation result of gain and phase of the OTA before and after applying the feed-forward technique.

Table 1. Comparison results for before and after applying the feed-forward technique.

| | Proposed OTA | Conventional OTA | [7] | [8] | [9] | [10] |
|----------------------|--------------|------------------|-------|------|------|------|
| Gain (dB) | 88 | 78 | 88.5 | 62 | 90 | 60 |
| UGB (Khz) | 86 | 26 | 83.88 | 540 | 113 | 1.88 |
| V _{dd} (V) | 0.4 | 0.4 | 0.5 | 0.9 | 1 | 0.25 |
| SR (mV/s) | 25 | 22 | 52 | N/A | N/A | 0.64 |
| P _{dc} (nW) | 386 | 386 | 1020 | 9900 | 8400 | 52 |
| FOM# | 49 | 13.1 | 14.47 | 3.76 | 1.21 | 8.68 |

(dB.kHz / V.nW)

4. CONCLUSION

A new feed-forward technique was introduced to improve OTA parameters. Employing idle devices in small signal path resulted in enhancing DC gain and unity gain bandwidth of the proposed OTA. The OTA represented gain and UGB of 88dB and 86 kHz, respectively, while those of the conventional OTA were 78dB and 26 kHz, respectively. In constant power dissipation, the applied technique was the simplest and the most efficient method to improve OTA parameters. The total power consumption of the OTA is as low as 386nW. This ultra-low power ultra-low voltage

architecture is very useful in bio-medical applications in which the power budget is limited.

ACKNOWLEDGMENT

The author would like to thank Ehsan Kargar from University of Pavia, Italy for faithful discussion.

REFERENCES

[1] P. E. Allen and D. R. Holberg, "CMOS Analog Circuits Design", 2nd ed. Oxford, U.K.: Oxford Univ. Press, 2002.

- [2] Luis H. C. Ferreira, T. C. Pimenta and R. L. Moreno, "An ultra-low voltage ultra-low-power CMOS Miller OTA with rail-to-rail input/output swing," *IEEE Trans. Circuits Syst. II, Exp. Briefs*, Vol. 54, No. 10, pp. 843-847, Oct. 2007.
- [3] E. Kargaran, M. Sawan, Kh. Mafinezhad, H. Nabovati, "Design of 0.4V,386nW OTA Using DTMOS Technique for Biomedical Applications," in *the proceeding of IEEE 55th International Midwest Symposium on Circuits and Systems (MWSCAS)*, USA, 2012.
- [4] B.k. Ahuja, "An improved frequency compensation technique for CMOS operational Amplifier," *IEEE Journal of Solid state circuits*, Vol. SC-18, No. 6, December 1983.
- [5] E. Kargaran, N. Zoka, Y. Mafinejad, A.Z. Kouzani, Kh. Mafinezhad, "An Ultra Low Power OTA with Improved Unity Gain Bandwidth Product," *IEICE Electron. Express*, October 2013.
- [6] H.F. Achigui, C.J. Fayomi, M. Sawan, "1 V DTMOS Based Class AB Operational Amplifier: Implementation and Experimental Results", *IEEE Journal of Solid-State Circuits*, Vol. 41, No. 11, November 2006, pp. 2440-2448.
- [7] M. Razzaghpour, A. Golmakani, "An Ultra-Low-Voltage Ultra-Low-Power OTA with Improved Gain-Bandwidth Product," in *proc. IEEE ICM*, 2008.
- [8] Sheng-Wen Pan, Chiung-Cheng Chuang, Chung-Huang Yang, Yu-Sheng Lai, "A Novel OTA with Dual Bulk- Driven Input Stage," *IEEE International Symposium on Circuits and Systems*, pp. 2721- 2724, May 2009.
- [9] S. Sbaraini, A. Richelli and Zs.M. Kovacs-Vajna, "EMI susceptibility in bulk-driven Miller opamp," *Electronics Letters*, Vol. 46, No. 16, August 2010.
- [10] Luis H. C. Ferreira, Sameer R. Sonkusale, "A 60-dB Gain OTA Operating at 0.25-V Power Supply in 130-nm Digital CMOS Process," *IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—I: REGULAR PAPERS*, 2014.